

Free download Computer architecture and organization miles murdocca (PDF)

an accessible introduction to computer systems and architecture anyone aspiring to more advanced studies in computer related fields must gain an understanding of the two parallel aspects of the modern digital computer programming methodology and the underlying machine architecture the uniquely integrated approach of computer architecture and organization connects the programmer's view of a computer system with the associated hardware and peripheral devices providing a thorough three dimensional view of what goes on inside the machine covering all the major topics normally found in a first course in computer architecture the text focuses on the essentials including the instruction set architecture isa network related issues and programming methodology using real world case studies to put the information into perspective the chapters examine data representation arithmetic the instruction set architecture datapath and control languages and the machine memory buses and peripherals networking and communication advanced computer architecture a valuable feature of this book is the use of arc a subset of the sparc processor for an instruction set architecture a platform independent arctools suite containing an assembler and simulator for the arc isa that supports the examples used in the book is available better yet the content is supplemented by online problem sets available through wileyplus balanced and thoughtfully designed for use as either a classroom text or self study guide computer architecture and organization an integrated approach will put you solidly on track for advancing to higher levels in computer related disciplines about the author miles murdoccaserves as the president and ceo of internet institute usa iiusa a private postsecondary information technology it school specializing in networking operating systems ip telephony programming and security previously dr murdocca has been a computer science faculty member at rutgers university and a research scientist at at t bell laboratories working in computer architecture networking and digital optical computing he is the author of a digital design methodology for optical computing and principles of computer architecture and a contributing author to computer systems design and architecture second edition as well as the author of dozens of professional papers and patents relating to information technology vince heuring is an associate professor and acting chair of the department of electrical and computer engineering at the university of colorado at boulder he has been at the university since 1984 and prior to that he spent three years at the university of cincinnati professor heuring's research encompasses computer architectures and programming language design implementation he and his colleague harry jordan designed and built the world's first stored program optical computer spoc in computing for ordinary mortals cognitive scientist and ai expert robert st amant explains what he calls the really interesting part of computing which are the ideas behind the technology they're powerful ideas and the foundations for everything that computers do but they are little discussed this book will not tell you how to use your computer but it will give you a conceptual tour of how it works some of the ideas like modularity which are so embedded in what we do as humans can also give us insight into our own daily activities how we interact with other people and in some cases even what's going on in our heads computing is all around us and to quote richard hamming the influential mathematician and computer scientist the purpose of computing is insight not numbers and it is this insight that informs the entire book the book uses microprocessors 8085 and above to explain the various concepts it not only covers the syllabi of most indian universities but also provides additional information about the latest developments like intel core ii duo making it one of the most updated textbook in the market the book has an excellent pedagogy sections like food for thought and quicksand corner make for an interesting read step by step guide to assembly language for the 64 bit itanium processors with extensive examples details of explicitly parallel instruction computing epic instruction set addressing register stack engine predication i/o procedure calls floating point operations and more learn how to comprehend and optimize open source intel and hp ux compiler output understand the full power of

64 bit itanium epic processors itanium r architecture for programmers is a comprehensive introduction to the breakthrough capabilities of the new 64 bit itanium architecture using standard command line tools and extensive examples the authors illuminate the itanium design within the broader context of contemporary computer architecture via a step by step investigation of itanium assembly language coverage includes the potential of explicitly parallel instruction computing epic itanium instruction formats and addressing modes innovations such as the register stack engine rse and extensive predication procedure calls and procedure calling mechanisms floating point operations i o techniques from simple debugging to the use of files optimization of output from open source intel and hp ux compilers an essential resource for both computing professionals and students of architecture or assembly language itanium architecture for programmers includes extensive printed and based references plus many numeric essay and programming exercises for each chapter designed especially for undergraduates in computer related disciplines with emphasis to support the compiler operating system and networking courses for a first course in computer architecture or computer organization this text covers computer architecture at the instruction set architecture isa and system design levels starting with foundation material on data representation and computer arithmetic the book moves through the basic components of a computer architecture covering topics at increasing levels of complexity up through cisc network architecture and parallel architecture the authors have adopted the use of a sparc subset for an instructional isa called arc a risc computer which is carried through the mainstream of the book and is complemented with platform independent software tools that simulate the arc isa as well as the mips and x86 pentium isas a computer is a machine designed for manipulating data according to a list of instructions known as a programme computers are versatile in fact they are universal information processing machines due to technological advancement modern electronic computers are exponentially more capable than those of preceding generations today computers are at the centre of thousands upon thousands of other inventions they are the heartbeats of the modern world computers are everywhere from kitchens to concrete mixers from planes to pockets they listen they speak they act never in world history has one invention had such an influence on humanity age there would be no global awareness today computers are being used in every walk of life and this book is useful to anyone who wishes to learn computers the first chapter traces the history of computers the rest of the book covers fundamental aspects such hardware software and other applications associated with computers computer science and engineering is a component of encyclopedia of technology information and systems management resources in the global encyclopedia of life support systems eolss which is an integrated compendium of twenty one encyclopedias the theme on computer science and engineering provides the essential aspects and fundamentals of hardware architectures software architectures algorithms and data structures programming languages and computer security it is aimed at the following five major target audiences university and college students educators professional practitioners research personnel and policy analysts managers and decision makers the concise encyclopedia of computer science has been adapted from the full fourth edition to meet the needs of students teachers and professional computer users in science and industry as an ideal desktop reference it contains shorter versions of 60 of the articles found in the fourth edition putting computer knowledge at your fingertips organised to work for you it has several features that make it an invaluable and accessible reference these include cross references to closely related articles to ensure that you don t miss relevant information appendices covering abbreviations and acronyms notation and units and a timeline of significant milestones in computing have been included to ensure that you get the most from the book a comprehensive index containing article titles names of persons cited references to sub categories and important words in general usage guarantees that you can easily find the information you need classification of articles around the following nine main themes allows you to follow a self study regime in a particular area hardware computer systems information and data software mathematics of computing theory of computation methodologies applications computing milieux presenting a wide ranging perspective on the key concepts and developments that define the discipline the concise encyclopedia of

computer science is a valuable reference for all computer users the roots of this book and of the new research field that it defines lie in the scaling of vlsi technology with gigahertz system clocks and ever accelerating design and process innovations interconnects have become the limiting factor for both performance and density this increasing impact of interconnects on the system implementation space necessitates new tools and analytic techniques to support the system designer with respect to modeling and analysis the response to interconnect dominance is evolutionary atomistic and grain level models of interconnect structure and performance models at multi gigahertz operating frequencies together guide the selection of improved materials and process technologies e g damascene copper wires low permittivity dielectrics previously in significant effects e g mutual inductance are added into performance models as older approximations e g lumped capacitance gate load models are discarded however at the system level and chip planning level the necessary response to interconnect dominance is revolutionary convergent design flows do not require only distributed rlc line models repeater awareness unifications with extraction and analysis etc rather issues such as wiring layer assignment and early prediction of the resource and performance envelope for the system interconnect in particular based on statistical models of the system interconnect structure also become critical indeed system level interconnect prediction has emerged as the enabler of improved interconnect modeling more cost effective system architectures and more productive design technology diploma thesis from the year 2007 in the subject computer science general grade 1 0 19 entries in the bibliography language english abstract the goal of this thesis is to analyse a common xforms framework chiba with the focus on profiling for performance inadequacies and to fix them if possible a prototype of a largely automated performance measurement setup to underpin the analysis and the verification of enhancements has been designed and implemented the thesis starts with a theoretical part to specify terms methodologies before the paper comes to its practical part the analysed project is introduced afterwards a practical performance analysis from the first steps to the narrow of the analysed project is exemplarily described in detail chapter 1 performance profiling elaborates how to quantify and profile performance in theory chapter 2 methods introduces various methods to analyse performance xforms and the analysed product chiba are presented in chapter 3 xforms chiba fundamentals the implemented performance analysis is described in chapter 4 performance analysis iteration and chapter 5 tuning xforms actions the performance analysis iteration focuses on the design and implementation of tests to analyse and validate potential performance shortages while tuning xforms actions illustrates how profiling techniques can be utilised not only to analyse but also to enhance a validated shortage finally in chapter 6 closing the results of the analysis and the enhancements are reconsidered this piece covers computer architecture at the instruction set architecture isa and system design levels starting with foundation material on data representation and computer arithmetic the book moves through the basic components of a computer architecture covering topics at increasing levels of complexity up through cisc network architecture and parallel architecture the authors have adopted the use of a sparc subset for an instructional isa called arc a risc computer which is carried through the mainstream of the book and is complemented with platform independent software tools that simulate the arc isa as well as the mips and x86 pentium isas features benefits choice of the instruction set architecture isa the mainstream isa arc is a subset of the commercial sparc which strikes a balance between the complexity of a real world architecture and the need for a simple instructional isa companion website prenhall com murdocca software available on companion website assembles and simulates program execution on sparc subset arc mips and intel isas simulators and assemblers run on pcs macs and unix over 400 adobe acrobat slides simplify lecture preparation password protected area of companion website case studies over 200 homework problems the major portion of the text deals with a high level look at computer architecture while the appendices and case studies cover lower level technology dependent aspects allows computer architecture to be studied at all levels spie milestones are collections of seminal papers from the world literature covering important discoveries and developments in optics and photonics this book presents a valuable new methodology for simplifying the design of digital

circuits for systems that use optics as an interconnection medium this book draws on foucault's concept of governmentality as a lens to analyze and critique how crime is understood reproduced and challenged presents an illustrated a z encyclopedia containing approximately 600 entries on computer and technology related topics includes annual cumulative index of inventors and patentees

a számítógépek alapjai c könyv a lehető legegyszerűbben és legérthetőbben tekinti át a számítógépek elvi felépítésének alapelveit figyelembe véve az alkalmazott korszerű megoldásokat hasznos információt találhatnak benne az elektronikai számítástechnikai és automatizálási szakmérnökök és technikusok valamint informatikusok s mindazok akik a számítástechnikai eszközök fejlesztésével gyártásával javításával és felhasználásával foglalkoznak ugyanakkor tankönyvként is felhasználható a technikus és felsőfokú szakképzésben valamint az egyetemi alap bsc és mesterképzésben msc a számítógépek alapjai c könyv számítástechnika hardver jellegű műszaki szakkönyv az általános bevezetés után a fixpontos és a lebegőpontos műveletvégzést valamint a műveletvégző egységeket tárgyalja majd a számítógépek huzalozott és mikroprogramozott vezérlésére vonatkozó ismereteket foglalja össze ezt követi a memóriák és a memóriakezelés bemutatása a jellegzetes szuperskaláris többszálás és többmagos processzorarchitektúrák áttekintése valamint a perifériavezérlők a sínek és a kötések tárgyalása következnek a perifériák a háttértárak a beviteli és a kiviteli eszközök valamint a számítógép hálózatok az osi modell alapján az ismertetés a számítógépek táplálási rendszereivel zárul a world list of books in the english language containing approximately 650 alphabetically arranged entries and 200 photographs the world of computer science meets the information need for a wide variety of computer studies it is a subject specific guide to pioneers discoveries theories concepts issues and ethics and gives attention to lesser known scientists minorities and women

1992

Computer Architecture and Organization

2007-03-16

an accessible introduction to computer systems and architecture anyone aspiring to more advanced studies in computer related fields must gain an understanding of the two parallel aspects of the modern digital computer programming methodology and the underlying machine architecture the uniquely integrated approach of computer architecture and organization connects the programmer's view of a computer system with the associated hardware and peripheral devices providing a thorough three dimensional view of what goes on inside the machine covering all the major topics normally found in a first course in computer architecture the text focuses on the essentials including the instruction set architecture isa network related issues and programming methodology using real world case studies to put the information into perspective the chapters examine data representation arithmetic the instruction set architecture datapath and control languages and the machine memory buses and peripherals networking and communication advanced computer architecture a valuable feature of this book is the use of arc a subset of the sparc processor for an instruction set architecture a platform independent arctools suite containing an assembler and simulator for the arc isa that supports the examples used in the book is available better yet the content is supplemented by online problem sets available through wileyplus balanced and thoughtfully designed for use as either a classroom text or self study guide computer architecture and organization an integrated approach will put you solidly on track for advancing to higher levels in computer related disciplines about the author miles murdoccaserves as the president and ceo of internet institute usa iiusa a private postsecondary information technology it school specializing in networking operating systems ip telephony programming and security previously dr murdocca has been a computer science faculty member at rutgers university and a research scientist at at t bell laboratories working in computer architecture networking and digital optical computing he is the author of a digital design methodology for optical computing and principles of computer architecture and a contributing author to computer systems design and architecture second edition as well as the author of dozens of professional papers and patents relating to information technology vince heuring is an associate professor and acting chair of the department of electrical and computer engineering at the university of colorado at boulder he has been at the university since 1984 and prior to that he spent three years at the university of cincinnati professor heuring's research encompasses computer architectures and programming language design implementation he and his colleague harry jordan designed and built the world's first stored program optical computer spoc

Computing for Ordinary Mortals

2013

in computing for ordinary mortals cognitive scientist and ai expert robert st amant explains what he calls the really interesting part of computing which are the ideas behind the technology they're powerful ideas and the foundations for everything that computers do but they are little discussed this book will not tell you how to use your computer but it will give you a conceptual tour of how it works some of the ideas like modularity which are so embedded in what we do as humans can also give us insight into our own daily activities how we interact with other people and in some cases even what's going on in our heads computing is all around us and to quote richard hamming the influential mathematician and computer scientist the purpose of computing is insight not numbers and it is this insight that informs the entire book

Computer Architecture and Organization

2007-03-22

the book uses microprocessors 8085 and above to explain the various concepts it not only covers the syllabi of most indian universities but also provides additional information about the latest developments like intel core ii duo making it one of the most updated textbook in the market the book has an excellent pedagogy sections like food for thought and quicksand corner make for an interesting read

Computer Architecture and Organization: From 8085 to core2Duo & beyond

2011

step by step guide to assembly language for the 64 bit itanium processors with extensive examples details of explicitly parallel instruction computing epic instruction set addressing register stack engine predication i o procedure calls floating point operations and more learn how to comprehend and optimize open source intel and hp ux compiler output understand the full power of 64 bit itanium epic processors itanium r architecture for programmers is a comprehensive introduction to the breakthrough capabilities of the new 64 bit itanium architecture using standard command line tools and extensive examples the authors illuminate the itanium design within the broader context of contemporary computer architecture via a step by step investigation of itanium assembly language coverage includes the potential of explicitly parallel instruction computing epic itanium instruction formats and addressing modes innovations such as the register stack engine rse and extensive predication procedure calls and procedure calling mechanisms floating point operations i o techniques from simple debugging to the use of files optimization of output from open source intel and hp ux compilers an essential resource for both computing professionals and students of architecture or assembly language itanium architecture for programmers includes extensive printed and based references plus many numeric essay and programming exercises for each chapter

Itanium Architecture for Programmers

2003

designed especially for undergraduates in computer related disciplines with emphasis to support the compiler operating system and networking courses for a first course in computer architecture or computer organization this text covers computer architecture at the instruction set architecture isa and system design levels starting with foundation material on data representation and computer arithmetic the book moves through the basic components of a computer architecture covering topics at increasing levels of complexity up through cisc network architecture and parallel architecture the authors have adopted the use of a sparc subset for an instructional isa called arc a risc computer which is carried through the mainstream of the book and is complemented with platform independent software tools that simulate the arc isa as well as the mips and x86 pentium isas

Principles of Computer Architecture

2000

a computer is a machine designed for manipulating data according to a list of instructions known as

a programme computers are versatile in fact they are universal information processing machines due to technological advancement modern electronic computers are exponentially more capable than those of preceding generations today computers are at the centre of thousands upon thousands of other inventions they are the heartbeats of the modern world computers are everywhere from kitchens to concrete mixers from planes to pockets they listen they speak they act never in world history has one invention had such an influence on humanity age there would be no global awareness today computers are being used in every walk of life and this book is useful to anyone who wishes to learn computers the first chapter traces the history of computers the rest of the book covers fundamental aspects such hardware software and other applications associated with computers

Introduction To Computers

2006-04

computer science and engineering is a component of encyclopedia of technology information and systems management resources in the global encyclopedia of life support systems eolss which is an integrated compendium of twenty one encyclopedias the theme on computer science and engineering provides the essential aspects and fundamentals of hardware architectures software architectures algorithms and data structures programming languages and computer security it is aimed at the following five major target audiences university and college students educators professional practitioners research personnel and policy analysts managers and decision makers

Computer Science and Engineering

2009-08-10

the concise encyclopedia of computer science has been adapted from the full fourth edition to meet the needs of students teachers and professional computer users in science and industry as an ideal desktop reference it contains shorter versions of 60 of the articles found in the fourth edition putting computer knowledge at your fingertips organised to work for you it has several features that make it an invaluable and accessible reference these include cross references to closely related articles to ensure that you don't miss relevant information appendices covering abbreviations and acronyms notation and units and a timeline of significant milestones in computing have been included to ensure that you get the most from the book a comprehensive index containing article titles names of persons cited references to sub categories and important words in general usage guarantees that you can easily find the information you need classification of articles around the following nine main themes allows you to follow a self study regime in a particular area hardware computer systems information and data software mathematics of computing theory of computation methodologies applications computing milieu presenting a wide ranging perspective on the key concepts and developments that define the discipline the concise encyclopedia of computer science is a valuable reference for all computer users

Computer Science

2004-09-03

the roots of this book and of the new research field that it defines lie in the scaling of vlsi technology with gigahertz system clocks and ever accelerating design and process innovations interconnects have become the limiting factor for both performance and density this increasing impact of interconnects on the system implementation space necessitates new tools and analytic techniques to

support the system designer with respect to modeling and analysis the response to interconnect dominance is evolutionary atomistic and grain level models of interconnect structure and performance models at multi gigahertz operating frequencies together guide the selection of improved materials and process technologies e g damascene copper wires low permittivity dielectrics previously in significant effects e g mutual inductance are added into performance models as older approximations e g lumped capacitance gate load models are discarded however at the system level and chip planning level the necessary response to interconnect dominance is revolutionary convergent design flows do not require only distributed rlc line models repeater awareness unifications with extraction and analysis etc rather issues such as wiring layer assignment and early prediction of the resource and performance envelope for the system interconnect in particular based on statistical models of the system interconnect structure also become critical indeed system level interconnect prediction has emerged as the enabler of improved interconnect modeling more cost effective system architectures and more productive design technology

Concise Encyclopedia of Computer Science

1994

diploma thesis from the year 2007 in the subject computer science general grade 1 0 19 entries in the bibliography language english abstract the goal of this thesis is to analyse a common xforms framework chiba with the focus on profiling for performance inadequacies and to fix them if possible a prototype of a largely automated performance measurement setup to underpin the analysis and the verification of enhancements has been designed and implemented the thesis starts with a theoretical part to specify terms methodologies before the paper comes to its practical part the analysed project is introduced afterwards a practical performance analysis from the first steps to the narrow of the analysed project is exemplarily described in detail chapter 1 performance profiling elaborates how to quantify and profile performance in theory chapter 2 methods introduces various methods to analyse performance xforms and the analysed product chiba are presented in chapter 3 xforms chiba fundamentals the implemented performance analysis is described in chapter 4 performance analysis iteration and chapter 5 tuning xforms actions the performance analysis iteration focuses on the design and implementation of tests to analyse and validate potential performance shortages while tuning xforms actions illustrates how profiling techniques can be utilised not only to analyse but also to enhance a validated shortage finally in chapter 6 closing the results of the analysis and the enhancements are reconsidered

Scientific and Technical Aerospace Reports

2011-06-27

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