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thermal motion 37 figure 2 1 the thermal motion of an electron or a hole changes direction frequently by chenming hu ch5 free download as pdf file pdf text file txt or read online for free hu chenming oral history catalog number 102746867 type moving image description dr hu describes a last minute escape from china to taiwan in 1949 where though poor he had what he describes as a wonderful childhood the standard cmos technology employs the 100 surface silicon wafers and the transistors are laid out so that the electrons and holes flow along the identical 0 1 1 directions on the wafer surface see section 1 1 for explanation of the notation professor hu chenming is an electronics engineer tsmc distinguished professor emeritus at the university of california uc berkeley and regarded internationally as the father of the 3d transistor cv of chenming hu education b s ee national taiwan university 1968 m s ph d university of california berkeley 1970 1973 experiences 1973 1976 assistant professor massachusetts institute of technology 1976 present professor of electrical engineering and computer sciences univ of california berkeley chenming hu ch2 free download as pdf file pdf text file txt or read online for free 1 the chapter discusses the motion and recombination of electrons and holes in semiconductors when disturbed by electric fields or concentration gradients

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the solution is of the form  $\exp(k \cdot r)$  which represents an electron wave  $k$  called the wave vector is equal at  $2 \pi$  electron wavelength and is a function of  $e$  in other words for each  $k$  there is a corresponding  $e$  see fig 4 27 for a schematic  $e$   $k$  diagram

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chapter 1 visualization of the silicon crystal 1 a please refer to figure 1 2 the 8 corner atoms are shared by 8 unit cells and therefore contribute 1 atom similarly the 6 face atoms are each shared by 2 unit cells and contribute 3 atoms and 4 atoms are located inside the unit cell

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this book is written for circuit designers and device engineers as well as device scientists worldwide it is also suitable as a reference for graduate courses and courses in circuit design or device modelling furthermore it can be used as a textbook for industry courses devoted to bsim3

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### *motion and recombination of electrons and holes chenming hu*

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he had what he describes as a wonderful childhood

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the standard cmos technology employs the 100 surface silicon wafers and the transistors are laid out so that the electrons and holes flow along the identical 0 1 1 directions on the wafer surface see section 1 1 for explanation of the notation

## ***hu chenming biography the honorary graduates hku***

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professor hu chenming is an electronics engineer tsmc distinguished professor emeritus at the university of california uc berkeley and regarded internationally as the father of the 3d transistor

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