

PDF FREE SOLUTION FAULT TOLERANT SYSTEMS KOREN DOWNLOAD [PDF]

FAULT-TOLERANT SYSTEMS FAULT-TOLERANT SYSTEMS ENERGY-EFFICIENT FAULT-TOLERANT SYSTEMS ENCYCLOPEDIA OF COMPUTER SCIENCE AND TECHNOLOGY DEFECT AND FAULT TOLERANCE IN VLSI SYSTEMS RESILIENT COMPUTER SYSTEM DESIGN CODING APPROACHES TO FAULT TOLERANCE IN COMBINATIONAL AND DYNAMIC SYSTEMS DEFECT AND FAULT TOLERANCE IN VLSI SYSTEMS DESIGN AND TEST TECHNOLOGY FOR DEPENDABLE SYSTEMS-ON-CHIP ADVANCES IN COMPUTER SCIENCE AND UBIQUITOUS COMPUTING FROM FAULT CLASSIFICATION TO FAULT TOLERANCE FOR MULTI-AGENT SYSTEMS QUALITY-OF-SERVICE AWARE DESIGN AND MANAGEMENT OF EMBEDDED MIXED-CRITICALITY SYSTEMS COMPUTER SAFETY, RELIABILITY, AND SECURITY INTEGRATED CIRCUIT MANUFACTURABILITY FORMAL TECHNIQUES IN REAL-TIME AND FAULT-TOLERANT SYSTEMS RESEARCH ANTHOLOGY ON ARCHITECTURES, FRAMEWORKS, AND INTEGRATION STRATEGIES FOR DISTRIBUTED AND CLOUD COMPUTING DISTRIBUTED COMPUTER AND COMMUNICATION NETWORKS PROCEEDINGS OF THE FIRST INTERNATIONAL SCIENTIFIC CONFERENCE "INTELLIGENT INFORMATION TECHNOLOGIES FOR INDUSTRY" (IIT'16) ADVANCES IN INFORMATION SECURITY AND ASSURANCE MODELING AND OPTIMIZATION OF PARALLEL AND DISTRIBUTED EMBEDDED SYSTEMS INTELLIGENT TECHNOLOGIES FOR INTERNET OF VEHICLES CYBERNETICS APPROACHES IN INTELLIGENT SYSTEMS MATERIAL-INTEGRATED INTELLIGENT SYSTEMS ARTIFICIAL INTELLIGENCE AND SOFT COMPUTING ENCYCLOPEDIA OF CLOUD COMPUTING VLSI, COMMUNICATION AND SIGNAL PROCESSING INTERACTIVE COLLABORATIVE ROBOTICS TELECOMMUNICATIONS IN DISASTER AREAS NANOTECHNOLOGY: CONCEPTS, METHODOLOGIES, TOOLS, AND APPLICATIONS DEFECT AND FAULT TOLERANCE IN VLSI SYSTEMS ADVANCED INFORMATION NETWORKING AND APPLICATIONS NEUROMORPHIC COMPUTING PRINCIPLES AND ORGANIZATION PROCEEDINGS OF THE 5TH INTERNATIONAL WORKSHOP ON RECONFIGURABLE COMMUNICATION-CENTRIC SYSTEMS ON CHIP 2010 - ReCoSoC'10 ROBUST COMPUTING WITH NANO-SCALE DEVICES APPROXIMATE COMPUTING AUTOMATIC SYNTHESIS OF FAULT-TOLERANT VLSI SYSTEMS PROCEEDINGS, THE IEEE INTERNATIONAL WORKSHOP ON DEFECT AND FAULT TOLERANCE IN VLSI SYSTEMS, NOVEMBER 13-15, 1995, LAFAYETTE, LOUISIANA ZUVERLÄSSIGKEIT MECHATRONISCHER SYSTEME ORGANIC AND PERVASIVE COMPUTING -- ARCS 2004 FAULT DETECTION, SUPERVISION AND SAFETY OF TECHNICAL PROCESSES 2003 (SAFEPROCESS 2003)

FAULT-TOLERANT SYSTEMS

2010-07-19

FAULT TOLERANT SYSTEMS IS THE FIRST BOOK ON FAULT TOLERANCE DESIGN WITH A SYSTEMS APPROACH TO BOTH HARDWARE AND SOFTWARE NO OTHER TEXT ON THE MARKET TAKES THIS APPROACH NOR OFFERS THE COMPREHENSIVE AND UP TO DATE TREATMENT THAT KOREN AND KRISHNA PROVIDE THIS BOOK INCORPORATES CASE STUDIES THAT HIGHLIGHT SIX DIFFERENT COMPUTER SYSTEMS WITH FAULT TOLERANCE TECHNIQUES IMPLEMENTED IN THEIR DESIGN A COMPLETE ANCILLARY PACKAGE IS AVAILABLE TO LECTURERS INCLUDING ONLINE SOLUTIONS MANUAL FOR INSTRUCTORS AND POWERPOINT SLIDES STUDENTS DESIGNERS AND ARCHITECTS OF HIGH PERFORMANCE PROCESSORS WILL VALUE THIS COMPREHENSIVE OVERVIEW OF THE FIELD THE FIRST BOOK ON FAULT TOLERANCE DESIGN WITH A SYSTEMS APPROACH COMPREHENSIVE COVERAGE OF BOTH HARDWARE AND SOFTWARE FAULT TOLERANCE AS WELL AS INFORMATION AND TIME REDUNDANCY INCORPORATED CASE STUDIES HIGHLIGHT SIX DIFFERENT COMPUTER SYSTEMS WITH FAULT TOLERANCE TECHNIQUES IMPLEMENTED IN THEIR DESIGN AVAILABLE TO LECTURERS IS A COMPLETE ANCILLARY PACKAGE INCLUDING ONLINE SOLUTIONS MANUAL FOR INSTRUCTORS AND POWERPOINT SLIDES

FAULT-TOLERANT SYSTEMS

2020-09-01

FAULT TOLERANT SYSTEMS SECOND EDITION IS THE FIRST BOOK ON FAULT TOLERANCE DESIGN UTILIZING A SYSTEMS APPROACH TO BOTH HARDWARE AND SOFTWARE NO OTHER TEXT TAKES THIS APPROACH OR OFFERS THE COMPREHENSIVE AND UP TO DATE TREATMENT THAT KOREN AND KRISHNA PROVIDE THE BOOK COMPREHENSIVELY COVERS THE DESIGN OF FAULT TOLERANT HARDWARE AND SOFTWARE USE OF FAULT TOLERANCE TECHNIQUES TO IMPROVE MANUFACTURING YIELDS AND DESIGN AND ANALYSIS OF NETWORKS INCORPORATING CASE STUDIES THAT HIGHLIGHT MORE THAN TEN DIFFERENT COMPUTER SYSTEMS WITH FAULT TOLERANCE TECHNIQUES IMPLEMENTED IN THEIR DESIGN THE BOOK INCLUDES CRITICAL MATERIAL ON METHODS TO PROTECT AGAINST THREATS TO ENCRYPTION SUBSYSTEMS USED FOR SECURITY PURPOSES THE TEXT S UPDATED CONTENT WILL HELP STUDENTS AND PRACTITIONERS IN ELECTRICAL AND COMPUTER ENGINEERING AND COMPUTER SCIENCE LEARN HOW TO DESIGN RELIABLE COMPUTING SYSTEMS AND HOW TO ANALYZE FAULT TOLERANT COMPUTING SYSTEMS DELIVERS THE FIRST BOOK ON FAULT TOLERANCE DESIGN WITH A SYSTEMS APPROACH OFFERS COMPREHENSIVE COVERAGE OF BOTH HARDWARE AND SOFTWARE FAULT TOLERANCE AS WELL AS INFORMATION AND TIME REDUNDANCY FEATURES FULLY UPDATED CONTENT PLUS NEW CHAPTERS ON FAILURE MECHANISMS AND FAULT TOLERANCE IN CYBER PHYSICAL SYSTEMS PROVIDES A COMPLETE ANCILLARY PACKAGE INCLUDING AN ON LINE SOLUTIONS MANUAL FOR INSTRUCTORS AND POWERPOINT SLIDES

ENERGY-EFFICIENT FAULT-TOLERANT SYSTEMS

2013-09-07

THIS BOOK DESCRIBES THE STATE OF THE ART IN ENERGY EFFICIENT FAULT TOLERANT EMBEDDED SYSTEMS IT COVERS THE ENTIRE PRODUCT LIFECYCLE OF ELECTRONIC SYSTEMS DESIGN ANALYSIS AND TESTING AND INCLUDES DISCUSSION OF BOTH CIRCUIT AND SYSTEM LEVEL APPROACHES READERS WILL BE ENABLED TO MEET THE CONFLICTING DESIGN OBJECTIVES OF ENERGY EFFICIENCY AND FAULT TOLERANCE FOR RELIABILITY GIVEN THE UP TO DATE TECHNIQUES PRESENTED

ENCYCLOPEDIA OF COMPUTER SCIENCE AND TECHNOLOGY

2009

PRESENTS AN ILLUSTRATED A Z ENCYCLOPEDIA CONTAINING APPROXIMATELY 600 ENTRIES ON COMPUTER AND TECHNOLOGY RELATED TOPICS

DEFECT AND FAULT TOLERANCE IN VLSI SYSTEMS

2012-12-06

THIS BOOK CONTAINS AN EDITED SELECTION OF PAPERS PRESENTED AT THE INTERNATIONAL WORKSHOP ON DEFECT AND FAULT TOLERANCE IN VLSI SYSTEMS HELD OCTOBER 6 7 1988 IN SPRINGFIELD MASSACHUSETTS OUR THANKS GO TO ALL THE CONTRIBUTORS AND ESPECIALLY THE MEMBERS OF THE PROGRAM COMMITTEE FOR THE DIFFICULT AND TIME CONSUMING WORK INVOLVED IN SELECTING THE PAPERS THAT WERE PRESENTED IN THE WORKSHOP AND REVIEWING THE PAPERS INCLUDED IN THIS BOOK THANKS ARE ALSO DUE TO THE IEEE COMPUTER SOCIETY IN PARTICULAR THE TECHNICAL COMMITTEE ON FAULT TOLERANT COMPUTING AND THE TECHNICAL COMMITTEE ON VLSI AND THE UNIVERSITY OF MASSACHUSETTS AT AMHERST FOR SPONSORING THE WORKSHOP AND TO THE NATIONAL SCIENCE FOUNDATION FOR SUPPORTING UNDER GRANT NUMBER MIP 8803418 THE KEYNOTE ADDRESS AND THE DISTRIBUTION OF THIS BOOK TO ALL WORKSHOP ATTENDEES THE OBJECTIVE OF THE WORKSHOP WAS TO BRING T OGETHER RESEARCHERS AND PRACTITION ERS FROM BOTH INDUSTRY AND ACADEMIA IN THE FIELD OF DEFECT TOLERANCE AND YIELD EN HA NCEMENT IN

VLSI TO DISCUSS THEIR MUTUAL INTERESTS IN DEFECT TOLERANT ARCHITECTURES AND MODELS FOR INTEGRATED CIRCUIT DEFECTS FAULTS AND YIELD PROGRESS IN THIS AREA WAS SLOWED DOWN BY THE PROPRIETARY NATURE OF YIELD RELATED DATA AND BY THE LACK OF APPROPRIATE FORUMS FOR DISSEMINATING SUCH INFORMATION THE GOAL OF THIS WORKSHOP WAS THEREFORE TO PROVIDE A FORUM FOR A DIALOGUE AND EXCHANGE OF VIEWS A FOLLOW UP WORKSHOP IN OCTOBER 1989 WITH C H STAPPER FROM IBM AND V K JAIN FROM THE UNIVERSITY OF SOUTH FLORIDA AS GENERAL CO CHAIRMEN IS BEING ORGANIZED

RESILIENT COMPUTER SYSTEM DESIGN

2015-04-15

THIS BOOK PRESENTS A PARADIGM FOR DESIGNING NEW GENERATION RESILIENT AND EVOLVING COMPUTER SYSTEMS INCLUDING THEIR KEY CONCEPTS ELEMENTS OF SUPPORTIVE THEORY METHODS OF ANALYSIS AND SYNTHESIS OF ICT WITH NEW PROPERTIES OF EVOLVING FUNCTIONING AS WELL AS IMPLEMENTATION SCHEMES AND THEIR PROTOTYPING THE BOOK EXPLAINS WHY NEW ICT APPLICATIONS REQUIRE A COMPLETE REDESIGN OF COMPUTER SYSTEMS TO ADDRESS CHALLENGES OF EXTREME RELIABILITY HIGH PERFORMANCE AND POWER EFFICIENCY THE AUTHORS PRESENT A COMPREHENSIVE TREATMENT FOR DESIGNING THE NEXT GENERATION OF COMPUTERS ESPECIALLY ADDRESSING SAFETY CRITICAL AUTONOMOUS REAL TIME MILITARY BANKING AND WEARABLE HEALTH CARE SYSTEMS

CODING APPROACHES TO FAULT TOLERANCE IN COMBINATIONAL AND DYNAMIC SYSTEMS

2012-12-06

CODING APPROACHES TO FAULT TOLERANCE IN COMBINATIONAL AND DYNAMIC SYSTEMS DESCRIBES CODING APPROACHES FOR DESIGNING FAULT TOLERANT SYSTEMS I E SYSTEMS THAT EXHIBIT STRUCTURED REDUNDANCY THAT ENABLES THEM TO DISTINGUISH BETWEEN CORRECT AND INCORRECT RESULTS OR BETWEEN VALID AND INVALID STATES SINCE REDUNDANCY IS EXPENSIVE AND COUNTER INTUITIVE TO THE TRADITIONAL NOTION OF SYSTEM DESIGN THE BOOK FOCUSES ON RESOURCE EFFICIENT METHODOLOGIES THAT AVOID EXCESSIVE USE OF REDUNDANCY BY EXPLOITING THE ALGORITHMIC DYNAMIC STRUCTURE OF A PARTICULAR COMBINATIONAL OR DYNAMIC SYSTEM THE FIRST PART OF CODING APPROACHES TO FAULT TOLERANCE IN COMBINATIONAL AND DYNAMIC SYSTEMS FOCUSES ON FAULT TOLERANT COMBINATIONAL SYSTEMS PROVIDING A REVIEW OF VON NEUMANN S CLASSICAL WORK ON PROBABILISTIC LOGICS INCLUDING SOME MORE RECENT WORK ON NOISY GATES AND DESCRIBING THE USE OF ARITHMETIC CODING AND ALGORITHM BASED FAULT TOLERANT SCHEMES IN ALGEBRAIC SETTINGS THE SECOND PART OF THE BOOK FOCUSES ON FAULT TOLERANCE IN DYNAMIC SYSTEMS CODING APPROACHES TO FAULT TOLERANCE IN COMBINATIONAL AND DYNAMIC SYSTEMS ALSO DISCUSSES HOW IN A DYNAMIC SYSTEM SETTING ONE CAN RELAX THE TRADITIONAL ASSUMPTION THAT THE ERROR CORRECTING MECHANISM IS FAULT FREE BY USING DISTRIBUTED ERROR CORRECTING MECHANISMS THE FINAL CHAPTER PRESENTS A METHODOLOGY FOR FAULT DIAGNOSIS IN DISCRETE EVENT SYSTEMS THAT ARE DESCRIBED BY PETRI NET MODELS CODING TECHNIQUES ARE USED TO QUICKLY DETECT AND IDENTIFY FAILURES FROM THE FOREWORD HADJICOSTIS HAS SIGNIFICANTLY EXPANDED THE SETTING TO PROCESSES OCCURRING IN MORE GENERAL ALGEBRAIC AND DYNAMIC SYSTEMS THE BOOK RESPONDS TO THE GROWING NEED TO HANDLE FAULTS IN COMPLEX DIGITAL CHIPS AND COMPLEX NETWORKED SYSTEMS AND TO CONSIDER THE EFFECTS OF FAULTS AT THE DESIGN STAGE RATHER THAN AFTERWARDS GEORGE VERGHESE MASSACHUSETTS INSTITUTE OF TECHNOLOGY CODING APPROACHES TO FAULT TOLERANCE IN COMBINATIONAL AND DYNAMIC SYSTEMS WILL BE OF INTEREST TO BOTH RESEARCHERS AND PRACTITIONERS IN THE AREA OF FAULT TOLERANCE SYSTEMS DESIGN AND CONTROL

DEFECT AND FAULT TOLERANCE IN VLSI SYSTEMS

2013-06-29

HIGHER CIRCUIT DENSITIES INCREASINGLY MORE COMPLEX APPLICATION OBJECTIVES AND ADVANCED PACKAGING TECHNOLOGIES HAVE SUHSTANTIALLY INCREASED THE NEED TO INCORPORATE DEFECT TOLERANCE AND FAULT TOLERANCE IN THE DESIGN OF VLSI AND WSI SYSTEMS THE GOALS OF DEFECT TOLERANCE AND FAULT TOLERANCE ARE YIELD ENHANCEMENT AND IMPROVED RELIAHILITY THE EMPHASIS ON THIS AREA HAS RESULTED IN A NEW FIELD OF INTERDISCIPLINARY SCIENTIFIC RESEARCH I N FACT ADVANCED METHODS OF DEFECT FAULT CONTROL AND TOLERANCE ARE RESULTING IN ENHANCED MANUFACTURABILITY AND PRODUCTIVITY OF INTEGRATED CIRCUIT CHIPS VI SI SYSTEMS AND WAFER SCALE INTEGRATED CIRCUITS IN 1987 DR W MOORE ORGANIZED AN INTERNATIONAL WORKSHOP ON DESIGNING FOR YIELD AT OXFORD UNIVERSITY EDITED PAPERS OF THAT WORKSHOP WERE PUBLISHED IN REFERENCE II THE PARTICIPANTS IN THAT WORKSHOP AGREED THAT MEETINGS OF THIS TYPE SHOULD HE CON TINUED PREFERAHLY ON A YEARLY HASIS IT WAS DR I KOREN WHO ORGANIZED THE IEEE INTER NATIONAL WORKSHOP ON DEFECT AND FAULT TOLERANCE IN VLSI SYSTEMS IN SPRINGFIELD MASSACHUSETTS THE NEXT YEAR SELECTED PAPERS FROM THAT WORKSHOP WERE PUBLISHED AS THE FIRST VOLUME OF THIS SERIES 21

DESIGN AND TEST TECHNOLOGY FOR DEPENDABLE SYSTEMS-ON-CHIP

2011-01-01

THIS BOOK COVERS ASPECTS OF SYSTEM DESIGN AND EFFICIENT MODELLING AND ALSO INTRODUCES VARIOUS FAULT MODELS AND FAULT MECHANISMS ASSOCIATED WITH DIGITAL CIRCUITS INTEGRATED INTO SYSTEM ON CHIP SOC MULTI PROCESSOR SYSTEM ON CHIP MPSOC OR NETWORK ON CHIP NOC

ADVANCES IN COMPUTER SCIENCE AND UBIQUITOUS COMPUTING

2017-12-19

THIS BOOK PRESENTS THE COMBINED PROCEEDINGS OF THE 12TH KIPS INTERNATIONAL CONFERENCE ON UBIQUITOUS INFORMATION TECHNOLOGIES AND APPLICATIONS CUTE 2017 AND THE 9TH INTERNATIONAL CONFERENCE ON COMPUTER SCIENCE AND ITS APPLICATIONS CSA2017 BOTH HELD IN TAICHUNG TAIWAN DECEMBER 18 20 2017 THE AIM OF THESE TWO MEETINGS WAS TO PROMOTE DISCUSSION AND INTERACTION AMONG ACADEMICS RESEARCHERS AND PROFESSIONALS IN THE FIELD OF UBIQUITOUS COMPUTING TECHNOLOGIES THESE PROCEEDINGS REFLECT THE STATE OF THE ART IN THE DEVELOPMENT OF COMPUTATIONAL METHODS INVOLVING THEORY ALGORITHMS NUMERICAL SIMULATION ERROR AND UNCERTAINTY ANALYSIS AND NOVEL APPLICATIONS OF NEW PROCESSING TECHNIQUES IN ENGINEERING SCIENCE AND OTHER DISCIPLINES RELATED TO UBIQUITOUS COMPUTING JAMES J JONG HYUK PARK RECEIVED PH D DEGREES IN GRADUATE SCHOOL OF INFORMATION SECURITY FROM KOREA UNIVERSITY KOREA AND GRADUATE SCHOOL OF HUMAN SCIENCES FROM WASEDA UNIVERSITY JAPAN FROM DECEMBER 2002 TO JULY 2007 DR PARK HAD BEEN A RESEARCH SCIENTIST OF R D INSTITUTE HANWHA S C CO LTD KOREA FROM SEPTEMBER 2007 TO AUGUST 2009 HE HAD BEEN A PROFESSOR AT THE DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING KYUNGNAM UNIVERSITY KOREA HE IS NOW A PROFESSOR AT THE DEPARTMENT OF COMPUTER SCIENCE AND ENGINEERING AND DEPARTMENT OF INTERDISCIPLINARY BIO IT MATERIALS SEOUL NATIONAL UNIVERSITY OF SCIENCE AND TECHNOLOGY SEOULTECH KOREA DR PARK HAS PUBLISHED ABOUT 200 RESEARCH PAPERS IN INTERNATIONAL JOURNALS AND CONFERENCES HE HAS BEEN SERVING AS CHAIR PROGRAM COMMITTEE OR ORGANIZING COMMITTEE CHAIR FOR MANY INTERNATIONAL CONFERENCES AND WORKSHOPS HE IS A STEERING CHAIR OF INTERNATIONAL CONFERENCES MUE FUTURETECH CSA CUTE UCAWSN WORLD IT CONGRESS JEJU HE IS EDITOR IN CHIEF OF HUMAN CENTRIC COMPUTING AND INFORMATION SCIENCES HCIS BY SPRINGER THE JOURNAL OF INFORMATION PROCESSING SYSTEMS JIPS BY KIPS AND JOURNAL OF CONVERGENCE JOC BY KIPS CSWRG HE IS ASSOCIATE EDITOR OF 14 INTERNATIONAL JOURNALS INCLUDING JOS JNCA SCN CJ AND SO ON IN ADDITION HE HAS BEEN SERVING AS A GUEST EDITOR FOR INTERNATIONAL JOURNALS BY SOME PUBLISHERS SPRINGER ELSEVIER JOHN WILEY OXFORD UNIV PRESS EMERALD INDERSCIENCE MDPI HE GOT THE BEST PAPER AWARDS FROM ISA 08 AND ITCS 11 CONFERENCES AND THE OUTSTANDING LEADERSHIP AWARDS FROM IEEE HPCC 09 ICA3PP 10 IEE ISPA 11 PDCAT 11 IEEE AINA 15 FURTHERMORE HE GOT THE OUTSTANDING RESEARCH AWARDS FROM THE SEOULTECH 2014 HIS RESEARCH INTERESTS INCLUDE IOT HUMAN CENTRIC UBIQUITOUS COMPUTING INFORMATION SECURITY DIGITAL FORENSICS VEHICULAR CLOUD COMPUTING MULTIMEDIA COMPUTING ETC HE IS A MEMBER OF THE IEEE IEEE COMPUTER SOCIETY KIPS AND KMMS VINCENZO LOIA BS 85 MS 87 PHD 89 IS FULL PROFESSOR OF COMPUTER SCIENCE HIS RESEARCH INTERESTS INCLUDE INTELLIGENT AGENTS AMBIENT INTELLIGENCE COMPUTATIONAL INTELLIGENCE CURRENTLY HE IS FOUNDER EDITOR IN CHIEF OF AMBIENT INTELLIGENCE AND HUMANIZED COMPUTING AND CO EDITOR IN CHIEF OF SOFTCOMPUTING SPRINGER VERLAG HE IS CHAIR OF THE TASK FORCES INTELLIGENT AGENTS AND AMBIENT INTELLIGENCE IEEE CIS ETTC HE HAS BEEN CHAIR THE EMERGENT TECHNICAL COMMITTEE EMERGENT TECHNOLOGY IEEE CIS SOCIETY AND VICE CHAIR OF INTELLIGENT SYSTEMS APPLICATIONS TECHNICAL COMMITTEE HE HAS BEEN AUTHOR OF MORE THAN 200 SCIENTIFIC WORKS EDITOR CO EDITOR OF 4 BOOKS 64 JOURNAL PAPERS 25 BOOK CHAPTERS AND 100 CONFERENCE PAPERS HE IS SENIOR MEMBER OF THE IEEE ASSOCIATE EDITOR OF IEEE TRANSACTIONS ON INDUSTRIAL INFORMATICS AND ASSOCIATE EDITOR OF IEEE TRANSACTIONS ON SYSTEMS MAN AND CYBERNETICS SYSTEMS MANY TIMES REVIEWERS FOR NATIONAL AND INTERNATIONAL PROJECTS DR LOIA IS ACTIVE IN THE RESEARCH DOMAIN OF AGENTS AMBIENT INTELLIGENCE COMPUTATIONAL INTELLIGENCE SMARTGRIDS DISTRIBUTED PLATFORM FOR ENRICH ADDED VALUE GANGMAN YI IN COMPUTER SCIENCES AT TEXAS A M UNIVERSITY USA IN 2007 AND DOCTORATE IN COMPUTER SCIENCES AT TEXAS A M UNIVERSITY USA IN 2011 IN MAY 2011 HE JOINED SYSTEM S W GROUP IN SAMSUNG ELECTRONICS SUWON KOREA HE JOINED THE DEPARTMENT OF COMPUTER SCIENCE ENGINEERING GANGNEUNG WONJU NATIONAL UNIVERSITY KOREA SINCE MARCH 2012 DR YI HAS BEEN RESEARCHED IN AN INTERDISCIPLINARY FIELD OF RESEARCHES HIS RESEARCH FOCUSES ESPECIALLY ON THE DEVELOPMENT OF COMPUTATIONAL METHODS TO IMPROVE UNDERSTANDING OF BIOLOGICAL SYSTEMS AND ITS BIG DATA DR YI ACTIVELY SERVES AS A MANAGING EDITOR AND REVIEWER FOR INTERNATIONAL JOURNALS AND CHAIR OF INTERNATIONAL CONFERENCES AND WORKSHOPS YUNSICK SUNG RECEIVED HIS B S DEGREE IN DIVISION OF ELECTRICAL AND COMPUTER ENGINEERING FROM PUSAN NATIONAL UNIVERSITY BUSAN KOREA IN 2004 HIS M S DEGREE IN COMPUTER ENGINEERING FROM DONGGUK UNIVERSITY SEOUL KOREA IN 2006 AND HIS PH D DEGREE IN GAME ENGINEERING FROM DONGGUK UNIVERSITY SEOUL KOREA IN 2012 HE WAS EMPLOYED AS A MEMBER OF THE RESEARCHER AT SAMSUNG ELECTRONICS BETWEEN 2006 AND 2009 HE WAS THE PLURAL PROFESSOR AT SHINHEUNG COLLEGE IN 2009 AND AT DONGGUK UNIVERSITY IN 2010 HIS MAIN RESEARCH INTERESTS ARE MANY TOPICS IN BRAIN COMPUTER INTERFACE PROGRAMMING BY DEMONSTRATION UBIQUITOUS COMPUTING AND REINFORCEMENT LEARNING HIS JOURNAL SERVICE EXPERIENCES IS ASSOCIATE EDITOR AT HUMAN CENTRIC COMPUTING AND INFORMATION SCIENCES SPRINGER 2015 CURRENT

FROM FAULT CLASSIFICATION TO FAULT TOLERANCE FOR MULTI-AGENT SYSTEMS

2013-03-21

FAULTS ARE A CONCERN FOR MULTI AGENT SYSTEMS MAS DESIGNERS ESPECIALLY IF THE MAS ARE BUILT FOR INDUSTRIAL OR MILITARY USE BECAUSE THERE MUST BE SOME GUARANTEE OF DEPENDABILITY SOME FAULT CLASSIFICATION EXISTS FOR CLASSICAL SYSTEMS AND IS USED TO DEFINE FAULTS WHEN DEPENDABILITY IS AT STAKE SUCH FAULT CLASSIFICATION MAY BE USED FROM THE BEGINNING OF THE SYSTEM S CONCEPTION TO DEFINE FAULT CLASSES AND SPECIFY WHICH TYPES OF FAULTS ARE EXPECTED THIS ONE MAY WANT TO USE FAULT CLASSIFICATION FOR MAS HOWEVER FROM FAULT CLASSIFICATION TO FAULT TOLERANCE FOR MULTI AGENT SYSTEMS ARGUES THAT WORKING WITH AUTONOMOUS AND PROACTIVE AGENTS IMPLIES A SPECIAL ANALYSIS OF THE FAULTS POTENTIALLY OCCURRING IN THE SYSTEM MOREOVER THE FIELD OF FAULT TOLERANCE FT PROVIDES NUMEROUS METHODS ADAPTED TO HANDLE DIFFERENT KINDS OF FAULTS SOME HANDLING METHODS HAVE BEEN STUDIED WITHIN THE MAS DOMAIN ADAPTING TO THEIR SPECIFICITIES AND CAPABILITIES BUT INCREASING THE LARGE AMOUNT OF FT METHODS THEREFORE UNLESS BEING AN EXPERT IN FAULT TOLERANCE IT IS DIFFICULT TO CHOOSE EVALUATE OR COMPARE FAULT TOLERANCE METHODS PREVENTING A LOT OF DEVELOPED APPLICATIONS FROM NOT ONLY TO BEING MORE PLEASANT TO USE BUT MORE IMPORTANTLY FROM AT LEAST BEING TOLERANT TO COMMON FAULTS FROM FAULT CLASSIFICATION TO FAULT TOLERANCE FOR MULTI AGENT SYSTEMS SHOWS THAT SPECIFICATION PHASE GUIDELINES AND FAULT HANDLER STUDIES CAN BE DERIVED FROM THE FAULT CLASSIFICATION EXTENSION MADE FOR MAS FROM THIS PERSPECTIVE FAULT CLASSIFICATION CAN BECOME A UNIFYING CONCEPT BETWEEN FAULT TOLERANCE METHODS IN MAS

QUALITY-OF-SERVICE AWARE DESIGN AND MANAGEMENT OF EMBEDDED MIXED-CRITICALITY SYSTEMS

2023-10-28

2023-02-26

4/11

THIS BOOK ADDRESSES THE CHALLENGES ASSOCIATED WITH EFFICIENT MIXED CRITICALITY MC SYSTEM DESIGN WE FOCUS ON APPLICATION ANALYSIS THROUGH EXECUTION TIME ANALYSIS AND TASK SCHEDULING ANALYSIS IN ORDER TO EXECUTE MORE LOW CRITICALITY TASKS IN THE SYSTEM I E IMPROVING THE QUALITY OF SERVICE QOS WHILE GUARANTEING THE CORRECT EXECUTION OF HIGH CRITICALITY TASKS FURTHER THIS BOOK ADDRESSES THE CHALLENGE OF ENHANCING QOS USING PARALLELISM IN MULTI PROCESSOR HARDWARE PLATFORMS

COMPUTER SAFETY, RELIABILITY, AND SECURITY

2022-08-24

THIS BOOK CONSTITUTES THE PROCEEDINGS OF THE 41ST INTERNATIONAL CONFERENCE ON COMPUTER SAFETY RELIABILITY AND SECURITY SAFECOMP 2022 WHICH TOOK PLACE IN MUNICH GERMANY IN SEPTEMBER 2022 THE 24 FULL PAPERS INCLUDED IN THIS VOLUME WERE CAREFULLY REVIEWED AND SELECTED FROM 93 SUBMISSIONS SAFECOMP HAS CONTRIBUTED TO THE PROGRESS OF THE STATE OF THE ART IN DEPENDABLE APPLICATION OF COMPUTERS IN SAFETY RELATED AND SAFETY CRITICAL SYSTEMS SAFECOMP IS AN ANNUAL EVENT COVERING THE STATE OF THE ART EXPERIENCE AND NEW TRENDS IN THE AREAS OF SAFETY SECURITY AND RELIABILITY OF CRITICAL COMPUTER APPLICATIONS

INTEGRATED CIRCUIT MANUFACTURABILITY

1998-10-30

INTEGRATED CIRCUIT MANUFACTURABILITY PROVIDES COMPREHENSIVE COVERAGE OF THE PROCESS AND DESIGN VARIABLES THAT DETERMINE THE EASE AND FEASIBILITY OF FABRICATION OR MANUFACTURABILITY OF CONTEMPORARY VLSI SYSTEMS AND CIRCUITS THIS BOOK PROGRESSES FROM SEMICONDUCTOR PROCESSING TO ELECTRICAL DESIGN TO SYSTEM ARCHITECTURE THE MATERIAL PROVIDES A THEORETICAL BACKGROUND AS WELL AS CASE STUDIES EXAMINING THE ENTIRE DESIGN FOR THE MANUFACTURING PATH FROM CIRCUIT TO SILICON EACH CHAPTER INCLUDES TUTORIAL AND PRACTICAL APPLICATIONS COVERAGE INTEGRATED CIRCUIT MANUFACTURABILITY ILLUSTRATES THE IMPLICATIONS OF MANUFACTURABILITY AT EVERY LEVEL OF ABSTRACTION INCLUDING THE EFFECTS OF DEFECTS ON THE LAYOUT THEIR MAPPING TO ELECTRICAL FAULTS AND THE CORRESPONDING APPROACHES TO DETECT SUCH FAULTS THE READER WILL BE INTRODUCED TO KEY PRACTICAL ISSUES NORMALLY APPLIED IN INDUSTRY AND USUALLY REQUIRED BY QUALITY PRODUCT AND DESIGN ENGINEERING DEPARTMENTS IN TODAY S DESIGN PRACTICES YIELD MANAGEMENT STRATEGIES EFFECTS OF SPOT DEFECTS INDUCTIVE FAULT ANALYSIS AND TESTING FAULT TOLERANT ARCHITECTURES AND MCM TESTING STRATEGIES THIS BOOK WILL SERVE DESIGN AND PRODUCT ENGINEERS BOTH FROM ACADEMIA AND INDUSTRY IT CAN ALSO BE USED AS A REFERENCE OR TEXTBOOK FOR INTRODUCTORY GRADUATE LEVEL COURSES ON MANUFACTURING

FORMAL TECHNIQUES IN REAL-TIME AND FAULT-TOLERANT SYSTEMS

2000

DISTRIBUTED SYSTEMS INTERTWINE WITH OUR EVERYDAY LIVES THE BENEFITS AND CURRENT SHORTCOMINGS OF THE UNDERPINNING TECHNOLOGIES ARE EXPERIENCED BY A WIDE RANGE OF PEOPLE AND THEIR SMART DEVICES WITH THE RISE OF LARGE SCALE IOT AND SIMILAR DISTRIBUTED SYSTEMS CLOUD BURSTING TECHNOLOGIES AND PARTIAL OUTSOURCING SOLUTIONS PRIVATE ENTITIES ARE ENCOURAGED TO INCREASE THEIR EFFICIENCY AND OFFER UNPARALLELED AVAILABILITY AND RELIABILITY TO THEIR USERS THE RESEARCH ANTHOLOGY ON ARCHITECTURES FRAMEWORKS AND INTEGRATION STRATEGIES FOR DISTRIBUTED AND CLOUD COMPUTING IS A VITAL REFERENCE SOURCE THAT PROVIDES VALUABLE INSIGHT INTO CURRENT AND EMERGENT RESEARCH OCCURRING WITHIN THE FIELD OF DISTRIBUTED COMPUTING IT ALSO PRESENTS ARCHITECTURES AND SERVICE FRAMEWORKS TO ACHIEVE HIGHLY INTEGRATED DISTRIBUTED SYSTEMS AND SOLUTIONS TO INTEGRATION AND EFFICIENT MANAGEMENT CHALLENGES FACED BY CURRENT AND FUTURE DISTRIBUTED SYSTEMS HIGHLIGHTING A RANGE OF TOPICS SUCH AS DATA SHARING WIRELESS SENSOR NETWORKS AND SCALABILITY THIS MULTI VOLUME BOOK IS IDEALLY DESIGNED FOR SYSTEM ADMINISTRATORS INTEGRATORS DESIGNERS DEVELOPERS RESEARCHERS ACADEMICIANS AND STUDENTS

RESEARCH ANTHOLOGY ON ARCHITECTURES, FRAMEWORKS, AND INTEGRATION STRATEGIES FOR DISTRIBUTED AND CLOUD COMPUTING

2021-01-25

THIS BOOK CONSTITUTES THE REFEREED PROCEEDINGS OF THE 20TH INTERNATIONAL CONFERENCE ON DISTRIBUTED AND COMPUTER AND COMMUNICATION NETWORKS DCCN 2017 HELD IN MOSCOW RUSSIA IN SEPTEMBER 2017 THE 39 FULL PAPERS AND THE TWO SHORT PAPERS WERE CAREFULLY REVIEWED AND SELECTED FROM 176 SUBMISSIONS THE PAPERS COVER THE FOLLOWING TOPICS COMPUTER AND COMMUNICATION NETWORKS ARCHITECTURE OPTIMIZATION CONTROL IN COMPUTER AND COMMUNICATION NETWORKS PERFORMANCE AND QOS QOE EVALUATION IN WIRELESS NETWORKS ANALYTICAL MODELING AND SIMULATION OF NEXT GENERATION COMMUNICATIONS SYSTEMS QUEUEING THEORY AND RELIABILITY THEORY APPLICATIONS IN COMPUTER NETWORKS WIRELESS 4G 5G NETWORKS CM AND MM WAVE RADIO TECHNOLOGIES RFID TECHNOLOGY AND ITS APPLICATION IN INTELLECTUAL TRANSPORTATION NETWORKS INTERNET OF THINGS WEARABLES AND APPLICATIONS OF DISTRIBUTED INFORMATION SYSTEMS PROBABILISTIC AND STATISTICAL MODELS IN INFORMATION SYSTEMS MATHEMATICAL MODELING OF HIGH TECH SYSTEMS MATHEMATICAL MODELING AND CONTROL PROBLEMS DISTRIBUTED AND CLOUD COMPUTING SYSTEMS BIG DATA ANALYTICS

DISTRIBUTED COMPUTER AND COMMUNICATION NETWORKS

2017-09-06

THIS VOLUME OF ADVANCES IN INTELLIGENT SYSTEMS AND COMPUTING CONTAINS PAPERS PRESENTED IN THE MAIN TRACK OF IITI 2016 THE FIRST INTERNATIONAL CONFERENCE ON INTELLIGENT INFORMATION TECHNOLOGIES FOR INDUSTRY HELD IN MAY 16 21 IN SOCHI RUSSIA THE CONFERENCE WAS JOINTLY CO ORGANIZED BY ROSTOV STATE TRANSPORT UNIVERSITY RUSSIA AND VSB TECHNICAL UNIVERSITY OF OSTRAVA CZECH REPUBLIC WITH THE PARTICIPATION OF RUSSIAN ASSOCIATION FOR ARTIFICIAL INTELLIGENCE RAAI AND RUSSIAN ASSOCIATION FOR FUZZY SYSTEMS AND SOFT COMPUTING RAFSSC THE VOLUME IS DEVOTED TO PRACTICAL MODELS AND INDUSTRIAL APPLICATIONS RELATED TO INTELLIGENT INFORMATION SYSTEMS THE CONFERENCE HAS BEEN A MEETING POINT FOR RESEARCHERS AND PRACTITIONERS TO ENABLE THE IMPLEMENTATION OF ADVANCED INFORMATION TECHNOLOGIES INTO VARIOUS INDUSTRIES NEVERTHELESS SOME THEORETICAL TALKS CONCERNING THE STATE OF THE ART IN INTELLIGENT SYSTEMS AND SOFT COMPUTING ARE INCLUDED IN THE PROCEEDINGS AS WELL

PROCEEDINGS OF THE FIRST INTERNATIONAL SCIENTIFIC CONFERENCE “INTELLIGENT INFORMATION TECHNOLOGIES FOR INDUSTRY” (IITI'16)

2016-05-10

WELCOME TO THE THIRD INTERNATIONAL CONFERENCE ON INFORMATION SECURITY AND ASSURANCE ISA 2009 ISA 2009 WAS THE MOST COMPREHENSIVE CONFERENCE FOCUSED ON THE VARIOUS ASPECTS OF ADVANCES IN INFORMATION SECURITY AND ASSURANCE THE CONCEPT OF SECURITY AND ASSURANCE IS EMERGING RAPIDLY AS AN EXCITING NEW PARADIGM TO PROVIDE RELIABLE AND SAFE LIFE SERVICES OUR CONFERENCE PROVIDES A CHANCE FOR ACADEMIC AND INDUSTRY PROFESSIONALS TO DISCUSS RECENT PROGRESS IN THE AREA OF COMMUNICATION AND NETWORKING INCLUDING MODELING SIMULATION AND NOVEL APPLICATIONS ASSOCIATED WITH THE UTILIZATION AND ACCEPTANCE OF COMPUTING DEVICES AND SYSTEMS ISA 2009 WAS A SUCCESSOR OF THE FIRST INTERNATIONAL WORKSHOP ON INFORMATION ASSURANCE IN NETWORKS IAN 2007 JEJU ISLAND KOREA DECEMBER 2007 AND THE SECOND INTERNATIONAL CONFERENCE ON INFORMATION SECURITY AND ASSURANCE ISA 2008 BUSAN KOREA APRIL 2008 THE GOAL OF THIS CONFERENCE IS TO BRING TOGETHER RESEARCHERS FROM ACADEMIA AND INDUSTRY AS WELL AS PRACTITIONERS TO SHARE IDEAS PROBLEMS AND SOLUTIONS RELATING TO THE MULTIFACETED ASPECTS OF INFORMATION TECHNOLOGY ISA 2009 CONTAINED RESEARCH PAPERS SUBMITTED BY RESEARCHERS FROM ALL OVER THE WORLD IN ORDER TO GUARANTEE HIGH QUALITY PROCEEDINGS WE PUT EXTENSIVE EFFORT INTO REVIEWING THE PAPERS ALL SUBMISSIONS WERE PEER REVIEWED BY AT LEAST THREE PROGRAM COMMITTEE MEMBERS AS WELL AS EXTERNAL REVIEWERS AS THE QUALITY OF THE SUBMISSIONS WAS QUITE HIGH IT WAS EXTREMELY DIFFICULT TO SELECT THE PAPERS FOR ORAL PRESENTATION AND PUBLICATION IN THE PROCEEDINGS OF THE CONFERENCE

ADVANCES IN INFORMATION SECURITY AND ASSURANCE

2009-06-18

THIS BOOK INTRODUCES THE STATE OF THE ART IN RESEARCH IN PARALLEL AND DISTRIBUTED EMBEDDED SYSTEMS WHICH HAVE BEEN ENABLED BY DEVELOPMENTS IN SILICON TECHNOLOGY MICRO ELECTRO MECHANICAL SYSTEMS MEMS WIRELESS COMMUNICATIONS COMPUTER NETWORKING AND DIGITAL ELECTRONICS THESE SYSTEMS HAVE DIVERSE APPLICATIONS IN DOMAINS INCLUDING MILITARY AND DEFENSE MEDICAL AUTOMOTIVE AND UNMANNED AUTONOMOUS VEHICLES THE EMPHASIS OF THE BOOK IS ON THE MODELING AND OPTIMIZATION OF EMERGING PARALLEL AND DISTRIBUTED EMBEDDED SYSTEMS IN RELATION TO THE THREE KEY DESIGN METRICS OF PERFORMANCE POWER AND DEPENDABILITY KEY FEATURES INCLUDES AN EMBEDDED WIRELESS SENSOR NETWORKS CASE STUDY TO HELP ILLUSTRATE THE MODELING AND OPTIMIZATION OF DISTRIBUTED EMBEDDED SYSTEMS PROVIDES AN ANALYSIS OF MULTI CORE MANY CORE BASED EMBEDDED SYSTEMS TO EXPLAIN THE MODELING AND OPTIMIZATION OF PARALLEL EMBEDDED SYSTEMS FEATURES AN APPLICATION METRICS ESTIMATION MODEL MARKOV MODELING FOR FAULT TOLERANCE AND ANALYSIS AND QUEUEING THEORETIC MODELING FOR PERFORMANCE EVALUATION DISCUSSES OPTIMIZATION APPROACHES FOR DISTRIBUTED WIRELESS SENSOR NETWORKS HIGH PERFORMANCE AND ENERGY EFFICIENT TECHNIQUES AT THE ARCHITECTURE MIDDLEWARE AND SOFTWARE LEVELS FOR PARALLEL MULTICORE BASED EMBEDDED SYSTEMS AND DYNAMIC OPTIMIZATION METHODOLOGIES HIGHLIGHTS RESEARCH CHALLENGES AND FUTURE RESEARCH DIRECTIONS THE BOOK IS PRIMARILY AIMED AT RESEARCHERS IN EMBEDDED SYSTEMS HOWEVER IT WILL ALSO SERVE AS AN INVALUABLE REFERENCE TO SENIOR UNDERGRADUATE AND GRADUATE STUDENTS WITH AN INTEREST IN EMBEDDED SYSTEMS RESEARCH

MODELING AND OPTIMIZATION OF PARALLEL AND DISTRIBUTED EMBEDDED SYSTEMS

2016-02-08

THIS BOOK GATHERS RECENT RESEARCH WORKS IN EMERGING ARTIFICIAL INTELLIGENCE AI METHODS FOR THE CONVERGENCE OF COMMUNICATION CACHING CONTROL AND COMPUTING RESOURCES IN CLOUD BASED INTERNET OF VEHICLES IOV INFRASTRUCTURES IN THIS CONTEXT THE BOOK'S MAJOR SUBJECTS COVER THE ANALYSIS AND THE DEVELOPMENT OF AI POWERED MECHANISMS IN FUTURE IOV APPLICATIONS AND ARCHITECTURES IT ADDRESSES THE MAJOR NEW TECHNOLOGICAL DEVELOPMENTS IN THE FIELD AND REFLECTS CURRENT RESEARCH TRENDS AND INDUSTRY NEEDS IT COMPRISES A GOOD BALANCE BETWEEN THEORETICAL AND PRACTICAL ISSUES COVERING CASE STUDIES EXPERIENCE AND EVALUATION REPORTS AND BEST PRACTICES IN UTILIZING AI APPLICATIONS IN IOV NETWORKS IT ALSO PROVIDES TECHNICAL SCIENTIFIC INFORMATION ABOUT VARIOUS ASPECTS OF AI TECHNOLOGIES RANGING FROM BASIC CONCEPTS TO RESEARCH GRADE MATERIAL INCLUDING FUTURE DIRECTIONS THIS BOOK IS INTENDED FOR RESEARCHERS PRACTITIONERS ENGINEERS AND SCIENTISTS INVOLVED IN DESIGNING AND DEVELOPING PROTOCOLS AND AI APPLICATIONS AND SERVICES FOR IOV RELATED DEVICES

INTELLIGENT TECHNOLOGIES FOR INTERNET OF VEHICLES

2021-06-09

THIS BOOK DISCUSSES NEW APPROACHES AND METHODS IN THE CYBERNETICS ALGORITHMS AND SOFTWARE ENGINEERING IN THE SCOPE OF THE INTELLIGENT SYSTEMS IT BRINGS NEW APPROACHES AND METHODS TO REAL WORLD PROBLEMS AND EXPLORATORY RESEARCH THAT DESCRIBES NOVEL APPROACHES IN THE CYBERNETICS ALGORITHMS AND SOFTWARE ENGINEERING IN THE SCOPE OF THE INTELLIGENT SYSTEMS THIS BOOK CONSTITUTES THE REFEREED PROCEEDINGS OF THE COMPUTATIONAL METHODS IN SYSTEMS AND SOFTWARE 2017 A CONFERENCE THAT PROVIDED AN INTERNATIONAL FORUM FOR THE DISCUSSION OF THE LATEST HIGH QUALITY RESEARCH RESULTS IN ALL AREAS RELATED TO COMPUTATIONAL METHODS STATISTICS CYBERNETICS AND SOFTWARE ENGINEERING

CYBERNETICS APPROACHES IN INTELLIGENT SYSTEMS

2017-09-04

COMBINING DIFFERENT PERSPECTIVES FROM MATERIALS SCIENCE ENGINEERING AND COMPUTER SCIENCE THIS REFERENCE PROVIDES A UNIFIED VIEW OF THE VARIOUS ASPECTS NECESSARY FOR THE SUCCESSFUL REALIZATION OF INTELLIGENT SYSTEMS THE EDITORS AND AUTHORS ARE FROM ACADEMIA AND RESEARCH INSTITUTIONS WITH CLOSE TIES TO INDUSTRY AND ARE THUS ABLE TO OFFER FIRST HAND INFORMATION HERE THEY ADOPT A UNIQUE THREE TIERED APPROACH SUCH THAT READERS CAN GAIN BASIC INTERMEDIATE AND ADVANCED TOPICAL KNOWLEDGE THE TECHNOLOGY SECTION OF THE BOOK IS DIVIDED INTO CHAPTERS COVERING THE BASICS OF SENSOR INTEGRATION IN MATERIALS THE CHALLENGES ASSOCIATED WITH THIS APPROACH DATA PROCESSING EVALUATION AND VALIDATION AS WELL AS METHODS FOR ACHIEVING AN AUTONOMOUS ENERGY SUPPLY THE APPLICATIONS PART THEN GOES ON TO SHOWCASE TYPICAL SCENARIOS WHERE MATERIAL INTEGRATED INTELLIGENT SYSTEMS ARE ALREADY IN USE SUCH AS FOR STRUCTURAL HEALTH MONITORING AND SMART TEXTILES

MATERIAL-INTEGRATED INTELLIGENT SYSTEMS

2018-03-12

THE TWO VOLUME SET LNAI 7267 AND 7268 TOGETHER WITH LNCS 7269 CONSTITUTES THE REFEREED PROCEEDINGS OF THE 11TH INTERNATIONAL CONFERENCE ON ARTIFICIAL INTELLIGENCE AND SOFT COMPUTING ICAISC 2012 HELD IN ZAKOPANE POLAND IN APRIL MAY 2012 THE 212 REVISED FULL PAPERS PRESENTED WERE CAREFULLY REVIEWED AND SELECTED FROM 483 SUBMISSIONS THE PAPERS ARE ORGANIZED IN TOPICAL SECTIONS ON NEURAL NETWORKS AND THEIR APPLICATIONS COMPUTER VISION IMAGE AND SPEECH ANALYSIS DATA MINING HARDWARE IMPLEMENTATION BIOINFORMATICS BIOMETRICS AND MEDICAL APPLICATIONS CONCURRENT PARALLEL PROCESSING AGENT SYSTEMS ROBOTICS AND CONTROL ARTIFICIAL INTELLIGENCE IN MODELING AND SIMULATION VARIOUS PROBLEMS OD ARTIFICIAL INTELLIGENCE

ARTIFICIAL INTELLIGENCE AND SOFT COMPUTING

2012-04-17

THE ENCYCLOPEDIA OF CLOUD COMPUTING PROVIDES IT PROFESSIONALS EDUCATORS RESEARCHERS AND STUDENTS WITH A COMPENDIUM OF CLOUD COMPUTING KNOWLEDGE AUTHORED BY A SPECTRUM OF SUBJECT MATTER EXPERTS IN INDUSTRY AND ACADEMIA THIS UNIQUE PUBLICATION IN A SINGLE VOLUME COVERS A WIDE RANGE OF CLOUD COMPUTING TOPICS INCLUDING TECHNOLOGICAL TRENDS AND DEVELOPMENTS RESEARCH OPPORTUNITIES BEST PRACTICES STANDARDS AND CLOUD ADOPTION PROVIDING MULTIPLE PERSPECTIVES IT ALSO ADDRESSES QUESTIONS THAT STAKEHOLDERS MIGHT HAVE IN THE CONTEXT OF DEVELOPMENT OPERATION MANAGEMENT AND USE OF CLOUDS FURTHERMORE IT EXAMINES CLOUD COMPUTING S IMPACT NOW AND IN THE FUTURE THE ENCYCLOPEDIA PRESENTS 56 CHAPTERS LOGICALLY ORGANIZED INTO 10 SECTIONS EACH CHAPTER COVERS A MAJOR TOPIC AREA WITH CROSS REFERENCES TO OTHER CHAPTERS AND CONTAINS TABLES ILLUSTRATIONS SIDE BARS AS APPROPRIATE FURTHERMORE EACH CHAPTER PRESENTS ITS SUMMARY AT THE BEGINNING AND BACKEND MATERIAL REFERENCES AND ADDITIONAL RESOURCES FOR FURTHER INFORMATION

ENCYCLOPEDIA OF CLOUD COMPUTING

2016-08-01

THIS BOOK COVERS A VARIETY OF TOPICS IN ELECTRONICS AND COMMUNICATION ENGINEERING ESPECIALLY IN THE AREA OF MICROELECTRONICS AND VLSI DESIGN COMMUNICATION SYSTEMS AND NETWORKS AND SIGNAL AND IMAGE PROCESSING THE CONTENT IS BASED ON PAPERS PRESENTED AT THE 5TH INTERNATIONAL CONFERENCE ON VLSI COMMUNICATION AND SIGNAL PROCESSING VCAS 2022 THE BOOK ALSO DISCUSSES THE EMERGING APPLICATIONS OF NOVEL TOOLS AND TECHNIQUES IN IMAGE VIDEO AND MULTIMEDIA SIGNAL PROCESSING THIS BOOK IS USEFUL TO STUDENTS RESEARCHERS AND PROFESSIONALS WORKING IN THE ELECTRONICS AND COMMUNICATION DOMAIN

VLSI, COMMUNICATION AND SIGNAL PROCESSING

2023-07-01

THIS BOOK CONSTITUTES THE REFEREED PROCEEDINGS OF THE 8TH INTERNATIONAL CONFERENCE ON INTERACTIVE COLLABORATIVE ROBOTICS ICR 2023 HELD IN BAKU AZERBAIJAN DURING OCTOBER 25 29 2023 THE 33 FULL PAPERS INCLUDED IN THIS BOOK WERE CAREFULLY REVIEWED AND SELECTED FROM 56 SUBMISSIONS THEY WERE ORGANIZED IN TOPICAL SECTIONS AS FOLLOWS FOCUSED THE FOUNDATIONS AND MEANS OF COLLABORATIVE BEHAVIOR OF ONE OR MORE ROBOTS PHYSICALLY INTERACTING WITH HUMAN IN OPERATIONAL ENVIRONMENTS CONFIGURED WITH EMBEDDED SENSOR NETWORKS AND CLOUD SERVICES UNDER UNCERTAINTY AND ENVIRONMENTAL VARIABILITY

INTERACTIVE COLLABORATIVE ROBOTICS

2023-09-04

COORDINATED RELIEF TO THE AFFECTED AREAS NEEDS TO BE GIVEN AS SOON AS POSSIBLE SO TO MINIMIZE FURTHER NEFARIOUS EFFECTS IN SUCH SCENARIOS IT IS VITAL THAT COMMUNICATIONS BETWEEN INTERESTED PARTIES I E RELIEF AND SECURITY GROUPS ARE ESTABLISHED AS QUICKLY AND AS EASILY AS POSSIBLE IDEALLY IN A PLUG PLAY OR ZERO CONFIGURATION FASHION

TELECOMMUNICATIONS IN DISASTER AREAS

2011-01-29

OVER THE PAST FEW DECADES DEVICES AND TECHNOLOGIES HAVE BEEN SIGNIFICANTLY MINIATURIZED FROM ONE GENERATION TO THE NEXT PROVIDING FAR MORE POTENTIAL IN A MUCH SMALLER PACKAGE THE SMALLEST OF THESE RECENTLY DEVELOPED TOOLS ARE MINISCALE ENOUGH TO BE INVISIBLE TO THE NAKED EYE NANOTECHNOLOGY CONCEPTS METHODOLOGIES TOOLS AND APPLICATIONS DESCRIBES SOME OF THE LATEST ADVANCES IN MICROSCOPIC TECHNOLOGIES IN FIELDS AS DIVERSE AS BIOCHEMISTRY MATERIALS SCIENCE MEDICINE AND ELECTRONICS THROUGH ITS INVESTIGATION OF THEORIES APPLICATIONS AND NEW DEVELOPMENTS IN THE NANOTECHNOLOGY FIELD THIS IMPRESSIVE REFERENCE SOURCE WILL SERVE AS A VALUABLE TOOL FOR RESEARCHERS ENGINEERS ACADEMICS AND STUDENTS ALIKE

NANOTECHNOLOGY: CONCEPTS, METHODOLOGIES, TOOLS, AND APPLICATIONS

2014-02-28

DFT 2004 SHOWCASES THE LATEST RESEARCH RESULTS IN THE IN THE FIELD OF DEFECT AND FAULT TOLERANCE IN VLSI SYSTEMS ITS PAPERS COVER YIELD DEFECT AND FAULT TOLERANCE ERROR CORRECTION AND CIRCUIT SYSTEM RELIABILITY AND DEPENDABILITY

DEFECT AND FAULT TOLERANCE IN VLSI SYSTEMS

2004

NETWORKS OF TODAY ARE GOING THROUGH A RAPID EVOLUTION AND THERE ARE MANY EMERGING AREAS OF INFORMATION NETWORKING AND THEIR APPLICATIONS HETEROGENEOUS NETWORKING SUPPORTED BY RECENT TECHNOLOGICAL ADVANCES IN LOW POWER WIRELESS COMMUNICATIONS ALONG WITH SILICON INTEGRATION OF VARIOUS FUNCTIONALITIES SUCH AS SENSING COMMUNICATIONS INTELLIGENCE AND ACTUATIONS ARE EMERGING AS A CRITICALLY IMPORTANT DISRUPTIVE COMPUTER CLASS BASED ON A NEW PLATFORM NETWORKING STRUCTURE AND INTERFACE THAT ENABLE NOVEL LOW COST AND HIGH VOLUME APPLICATIONS SEVERAL OF SUCH APPLICATIONS HAVE BEEN DIFFICULT TO REALIZE BECAUSE OF MANY INTERCONNECTIONS PROBLEMS TO FULFILL THEIR LARGE RANGE OF APPLICATIONS DIFFERENT KINDS OF NETWORKS NEED TO COLLABORATE AND WIRED AND NEXT GENERATION WIRELESS SYSTEMS SHOULD BE INTEGRATED IN ORDER TO DEVELOP HIGH PERFORMANCE COMPUTING SOLUTIONS TO PROBLEMS ARISING FROM THE COMPLEXITIES OF THESE NETWORKS THIS VOLUME COVERS THE THEORY DESIGN AND APPLICATIONS OF COMPUTER NETWORKS DISTRIBUTED COMPUTING AND INFORMATION SYSTEMS THE AIM OF THE VOLUME ADVANCED INFORMATION NETWORKING AND APPLICATIONS IS TO PROVIDE LATEST RESEARCH FINDINGS INNOVATIVE RESEARCH RESULTS METHODS AND DEVELOPMENT TECHNIQUES FROM BOTH THEORETICAL AND PRACTICAL PERSPECTIVES RELATED TO THE EMERGING AREAS OF INFORMATION NETWORKING AND APPLICATIONS

ADVANCED INFORMATION NETWORKING AND APPLICATIONS

2023-03-19

THIS BOOK FOCUSES ON NEUROMORPHIC COMPUTING PRINCIPLES AND ORGANIZATION AND HOW TO BUILD FAULT TOLERANT SCALABLE HARDWARE FOR LARGE AND MEDIUM SCALE SPIKING NEURAL NETWORKS WITH LEARNING CAPABILITIES IN ADDITION THE BOOK DESCRIBES IN A COMPREHENSIVE WAY THE ORGANIZATION AND HOW TO DESIGN A SPIKE BASED NEUROMORPHIC SYSTEM TO PERFORM NETWORK OF SPIKING NEURONS COMMUNICATION COMPUTING AND ADAPTIVE LEARNING FOR EMERGING AI APPLICATIONS THE BOOK BEGINS WITH AN OVERVIEW OF NEUROMORPHIC COMPUTING SYSTEMS AND EXPLORES THE FUNDAMENTAL CONCEPTS OF ARTIFICIAL NEURAL NETWORKS NEXT WE DISCUSS ARTIFICIAL NEURONS AND HOW THEY HAVE EVOLVED IN THEIR REPRESENTATION OF BIOLOGICAL NEURONAL DYNAMICS AFTERWARD WE DISCUSS IMPLEMENTING THESE NEURAL NETWORKS IN NEURON MODELS STORAGE TECHNOLOGIES INTER NEURON COMMUNICATION NETWORKS LEARNING AND VARIOUS DESIGN APPROACHES THEN COMES THE FUNDAMENTAL DESIGN PRINCIPLE TO BUILD AN EFFICIENT NEUROMORPHIC SYSTEM IN HARDWARE THE CHALLENGES THAT NEED TO BE SOLVED TOWARD BUILDING A SPIKING NEURAL NETWORK ARCHITECTURE WITH MANY SYNAPSES ARE DISCUSSED LEARNING IN NEUROMORPHIC COMPUTING SYSTEMS AND THE MAJOR EMERGING MEMORY TECHNOLOGIES THAT PROMISE NEUROMORPHIC COMPUTING ARE THEN GIVEN A PARTICULAR CHAPTER OF THIS BOOK IS DEDICATED TO THE CIRCUITS AND ARCHITECTURES USED FOR COMMUNICATION IN NEUROMORPHIC SYSTEMS IN PARTICULAR THE NETWORK ON CHIP FABRIC IS INTRODUCED FOR RECEIVING AND TRANSMITTING SPIKES FOLLOWING THE ADDRESS EVENT REPRESENTATION AER PROTOCOL AND THE MEMORY ACCESSING METHOD IN ADDITION THE INTERCONNECT DESIGN PRINCIPLE IS COVERED TO HELP UNDERSTAND THE OVERALL CONCEPT OF ON CHIP AND OFF CHIP COMMUNICATION ADVANCED ON CHIP INTERCONNECT TECHNOLOGIES INCLUDING SI PHOTONIC THREE DIMENSIONAL INTERCONNECTS AND FAULT TOLERANT ROUTING ALGORITHMS ARE ALSO GIVEN THE BOOK ALSO COVERS THE MAIN THREATS OF RELIABILITY AND DISCUSSES SEVERAL RECOVERY METHODS FOR MULTICORE NEUROMORPHIC SYSTEMS THIS IS IMPORTANT FOR RELIABLE PROCESSING IN SEVERAL EMBEDDED NEUROMORPHIC APPLICATIONS A RECONFIGURABLE DESIGN APPROACH THAT SUPPORTS MULTIPLE TARGET APPLICATIONS VIA DYNAMIC RECONFIGURABILITY NETWORK TOPOLOGY INDEPENDENCE AND NETWORK EXPANDABILITY IS ALSO DESCRIBED IN THE SUBSEQUENT CHAPTERS THE BOOK ENDS WITH A CASE STUDY ABOUT A REAL HARDWARE SOFTWARE DESIGN OF A RELIABLE THREE DIMENSIONAL DIGITAL NEUROMORPHIC PROCESSOR GEARED EXPLICITLY TOWARD THE 3D ICS BIOLOGICAL BRAIN S THREE DIMENSIONAL STRUCTURE THE PLATFORM ENABLES HIGH INTEGRATION DENSITY AND SLIGHT SPIKE DELAY OF SPIKING NETWORKS AND FEATURES A SCALABLE DESIGN WE PRESENT METHODS FOR FAULT DETECTION AND RECOVERY IN A NEUROMORPHIC SYSTEM AS WELL NEUROMORPHIC COMPUTING PRINCIPLES AND ORGANIZATION IS AN EXCELLENT RESOURCE FOR RESEARCHERS SCIENTISTS GRADUATE STUDENTS AND HARDWARE SOFTWARE ENGINEERS DEALING WITH THE EVER INCREASING DEMANDS ON FAULT TOLERANCE SCALABILITY AND LOW POWER CONSUMPTION IT IS ALSO AN EXCELLENT RESOURCE FOR TEACHING ADVANCED UNDERGRADUATE AND GRADUATE STUDENTS ABOUT THE FUNDAMENTALS CONCEPTS ORGANIZATION AND ACTUAL HARDWARE SOFTWARE DESIGN OF RELIABLE NEUROMORPHIC SYSTEMS WITH LEARNING AND FAULT TOLERANCE CAPABILITIES

NEUROMORPHIC COMPUTING PRINCIPLES AND ORGANIZATION

2022-05-31

ROBUST NANO COMPUTING FOCUSES ON VARIOUS ISSUES OF ROBUST NANO COMPUTING DEFECT TOLERANCE DESIGN FOR NANO TECHNOLOGY AT DIFFERENT DESIGN ABSTRACTION LEVELS IT ADDRESSES BOTH REDUNDANCY AND CONFIGURATION BASED METHODS AS WELL AS FAULT DETECTING TECHNIQUES THROUGH THE DEVELOPMENT OF ACCURATE COMPUTATION MODELS AND TOOLS THE CONTENTS PRESENT AN INSIGHTFUL VIEW OF THE ONGOING RESEARCHES ON NANO ELECTRONIC DEVICES CIRCUITS ARCHITECTURES AND DESIGN METHODS AS WELL AS PROVIDE PROMISING DIRECTIONS FOR FUTURE RESEARCH

PROCEEDINGS OF THE 5TH INTERNATIONAL WORKSHOP ON RECONFIGURABLE COMMUNICATION-CENTRIC SYSTEMS ON CHIP 2010 - ReCoSoC'10

2010

THIS BOOK EXPLORES THE TECHNOLOGICAL DEVELOPMENTS AT VARIOUS LEVELS OF ABSTRACTION OF THE NEW PARADIGM OF APPROXIMATE COMPUTING THE AUTHORS DESCRIBE IN A SINGLE SOURCE THE STATE OF THE ART COVERING THE ENTIRE SPECTRUM OF RESEARCH ACTIVITIES IN APPROXIMATE COMPUTING BRIDGING DEVICE CIRCUIT ARCHITECTURE AND SYSTEM LEVELS CONTENT INCLUDES TUTORIALS REVIEWS AND SURVEYS OF CURRENT THEORETICAL EXPERIMENTAL RESULTS DESIGN METHODOLOGIES AND APPLICATIONS DEVELOPED IN APPROXIMATE COMPUTING FOR A WIDE SCOPE OF READERSHIP AND SPECIALISTS SERVES AS A SINGLE SOURCE REFERENCE TO STATE OF THE ART OF APPROXIMATE COMPUTING COVERS BROAD RANGE OF TOPICS FROM CIRCUITS TO APPLICATIONS INCLUDES CONTRIBUTIONS BY LEADING RESEARCHERS FROM ACADEMIA AND INDUSTRY

ROBUST COMPUTING WITH NANO-SCALE DEVICES

2010-03-11

AN INVITED TALK RECOUNTS INTEL S EXPERIENCE WITH INCREASING DIE YIELD THROUGH CAD ALGORITHMS AND A PANEL DISCUSSION EXAMINES TOOLS FOR THE EXTRACTING OF CRITICAL AREAS FOR A YIELD ANALYSIS OF VLSI DESIGN OTHERS OF THE 34 PAPERS COVER CRITICAL AREA ANALYSIS DEFECT SENSITIVITY AND RELIABILITY FAULT TOLERANT ARCHITECTURES AND ARRAYS YIELD PROJECTION AND ENHANCEMENT FAULT TOLERANT AND TESTING TECHNIQUES AND SELF CHECKING AND CODING TECHNIQUES NO SUBJECT INDEX ANNOTATION COPYRIGHT BY BOOK NEWS INC PORTLAND OR

APPROXIMATE COMPUTING

2022-08-22

DAS BUCH BEHANDELT DIE ZUVERLÄSSIGKEITSBEWERTUNG MECHATRONISCHER SYSTEME SPEZIELL IN FÜR DEN ENTWICKLUNGSPHASEN HERAUSFORDERUNG HIERBEI IST VOR ALLEM DIE GANZHEITLICHE BETRACHTUNG DER DOMINANTEN MECHANIK ELEKTRONIK UND SOFTWARE SOWIE DER UNSICHEREN BZW UNVOLLSTÄNDIGEN DATEN NEBEN DER DOMINANTEN BERGREIFENDEN BETRACHTUNGSWEISE VERTIEFEN DIE AUTOREN EINZELNE THEMENASPEKTE DIE DER ZUVERLÄSSIGKEITSBEWERTUNG IN FÜR DEN ENTWICKLUNGSPHASEN DIENEN

AUTOMATIC SYNTHESIS OF FAULT-TOLERANT VLSI SYSTEMS

1993

WHERE IS SYSTEM ARCHITECTURE HEADING THE SPECIAL INTEREST GROUP ON COMPUTER AND SYSTEMS ARCHITECTURE FACHAUSSCHUSS RECHNER UND SYSTEMARCHITEKTUR OF THE GERMAN COMPUTER AND INFORMATION TECHNOLOGY ASSOCIATIONS GI AND ITG A DED THIS QUESTION AND DISCUSSED IT DURING TWO FUTURE WORKSHOPS IN 2002 THE RESULT IN A NUTSHELL EVERYTHING WILL CHANGE BUT EVERYTHING ELSE WILL REMAIN FUTURE SYSTEMS TECHNOLOGIES WILL BUILD ON A MATURE BASIS OF SILICON AND IC TECHNOLOGY ON WELL UNDERSTOOD PROGRAMMING LANGUAGES AND SOFTWARE ENGINEERING TECHNIQUES AND ON WELL ESTABLISHED OPERATING SYSTEMS AND MIDDLEWARE CONCEPTS NEWER AND STILL EXOTIC BUT EXCITING TECHNOLOGIES LIKE QUANTUM COMPUTING AND DNA PROCESSING ARE TO BE WATCHED CLOSELY BUT THEY WILL NOT BE MAINSTREAM IN THE NEXT DECADE ALTHOUGH THERE WILL BE CONSIDERABLE PROGRESS IN THESE BASIC TECHNOLOGIES IS THERE ANY MAJOR TREND WHICH UNIFIES THESE DIVERSE DEVELOPMENTS THERE IS A COMMON DENOMINATOR ACCORDING TO THE RESULT OF THE TWO FUTURE WORKSHOPS WHICH MARKS A NEW QUALITY THE CHALLENGE FOR FUTURE SYSTEMS TECHNOLOGIES LIES IN THE MASTERING OF COMPLEXITY RIGID AND INFLEXIBLE SYSTEMS BUILT UNDER A STRICT TOP DOWN REGIME HAVE REACHED THE LIMITS OF MANAGEABLE COMPLEXITY AS HAS BECOME OBVIOUS BY THE RECENT FAILURE OF SEVERAL LARGE SCALE PROJECTS NATURE IS THE MOST COMPLEX SYSTEM WE KNOW AND SHE HAS SOLVED THE PROBLEM SOMEHOW WE JUST HAVEN'T UNDERSTOOD EXACTLY HOW NATURE DOES IT BUT IT IS CLEAR THAT SYSTEMS DESIGNED BY NATURE LIKE AN ANTHILL OR A BEEHIVE OR A SWARM OF BIRDS OR A CITY ARE DIFFERENT FROM TODAY'S TECHNICAL SYSTEMS THAT HAVE BEEN DESIGNED BY ENGINEERS AND COMPUTER SCIENTISTS

PROCEEDINGS, THE IEEE INTERNATIONAL WORKSHOP ON DEFECT AND FAULT TOLERANCE IN VLSI SYSTEMS, NOVEMBER 13-15, 1995, LAFAYETTE, LOUISIANA

1995

A THREE VOLUME WORK BRINGING TOGETHER PAPERS PRESENTED AT SAFEPROCESS 2003 INCLUDING FOUR PLENARY PAPERS ON STATISTICAL PHYSICAL MODEL BASED AND LOGICAL MODEL BASED APPROACHES TO FAULT DETECTION AND DIAGNOSIS AS WELL AS 178 REGULAR PAPERS

ZUVERLÄSSIGKEIT MECHATRONISCHER SYSTEME

2009-02-17

ORGANIC AND PERVASIVE COMPUTING -- ARCS 2004

2004-02-12

FAULT DETECTION, SUPERVISION AND SAFETY OF TECHNICAL PROCESSES 2003 (SAFEPROCESS 2003)

2004-02-27

2023-02-26

10/11

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